

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): H. YAMADA, et al

Serial No.: Not yet assigned

Filed: March 12, 2004

For: MICROPROCESSOR, AND GRAPHICS PROCESSING APPARATUS
AND METHOD USING THE SAME

**INFORMATION DISCLOSURE STATEMENT
UNDER 37 CFR §1.97 & 1.98**

MS DD

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

March 12, 2004

Sir:

In the matter of the above-identified application, this Information Disclosure Statement is being submitted with the following citation as specified in 37 CFR §1.97(d).

"A copy of any patent, publication or other information listed in an Information Disclosure Statement is not required to be provided if it was previously cited by or submitted to the Office in a prior application, provided that the prior application is properly identified in the statement and relied upon for an earlier filing date under 35 U.S.C. §120."

Applicant(s) are submitting herewith a copy of Form PTO-1449 which list documents cited in parent application(s) Serial No. 09/414945, filed October 8, 1999; which is a continuation of application Serial No. 07/511,778, filed April 20, 1990, now U.S. Patent No. 6,229,543.

It is respectfully requested that this information disclosure statement be considered by the Examiner.

Please charge any shortage in the fees due in connection with the filing of this paper, including extension of time fees, to the deposit account of Antonelli, Terry, Stout & Kraus Deposit Account No. 01-2135 (566.28851CX2) please credit any excess fees to such deposit account.

Respectfully submitted,



Carl I. Brundidge
Registration No. 29,621
ANTONELLI, TERRY, STOUT & KRAUS, LLP

CIB/jdc
(703) 312-6600

FORM PTO-1449 U.S. Department of
Commerce (Rev. 4/92) Patent and Trademark Office

**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

(Use several sheets if necessary)

ATTY. DOCKET NO.

566.28851CX2

SERIAL NO.

Not yet assigned

APPLICANT
YAMADA, et al

FILING DATE
March 12, 2004

GROUP
Not yet assigned

U.S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLAS S	FILING DATE IF APPROPRIATE
	4 9 1 8 6 2 6	4/90	Watkins et al	364	518x	12/87
	4 9 3 0 0 8 7	5/90	Egawa et al	364	518	8/87
	4 9 4 7 3 4 2	8/90	Katsura et al	364	518	9/85
	4 7 3 3 3 0 5	3/88	Miura et al			
	4 7 4 9 9 9 0	6/88	Birkner			
	5 0 4 6 0 2 3	9/91	Katsura et al	395	162x	10/87
	4 2 5 0 5 4 5	2/81	Blahut, et al			
	DOCUMENT NUMBER	DATE	COUNTRY	CLA SS	SUBC LASS	ABSTRACT YES NO
	8 7 0 0 3 2 9	1/87	PCT			
	0 1 9 6 7 5 1	10/86	European			
	0 2 7 9 2 2 9	8/88	European			
5	7 0 2 3 1 5 0	5/82	Japan			Eng.Ab st.
6	2 1 2 3 5 7 1	6/87	Japan			Eng.Ab st.
5	8 0 1 0 2 4 3	1/83	Japan			XX
6	3 1 6 3 3 9 1	7/88	Japan			xx
6	4 3 1 2 2 8	2/89	Japan			xx
6	3 2 5 7 0 5 2	10/98	Japan			XX

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

	Toshiba Review, 43rd vol. 12 Issue, 1988, pp. 932-935.
	Digital Signal Processing IC Helps to Shed New Light on Image Processing Applications, P. Casey, et al, 2328 Electronic Design, vol. 34, March 20, No. 7, Hasbrouk Heights, NJ.

EXAMINER

DATE CONSIDERED

EXAMINER: Initial if citation is considered, draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

(Form PTO-1449 [6-4])

FORM PTO-1449 U.S. Department of
Commerce(Rev. 4/92) Patent and Trademark Office

**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

(Use several sheets if necessary)

ATTY. DOCKET NO.

566.28851CX2

SERIAL NO.

Not yet assigned

APPLICANT
YAMADA, et al

FILING DATE
March 12, 2004

GROUP
Not yet assigned

U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER							DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE	
		4	8	7	0	4	0	6	2/87	Gupta et al	345	506		
		4	9	0	3	2	1	7	2/97	Gupta et al	345	545		
		4	9	1	6	3	0	1	2/87	Mansfield et al	345	440		
		6	2	2	9	5	4	3	8/01	Yamada				
		DOCUMENT NUMBER							DATE	COUNTRY	CLASS	SUBCLASS	ABSTRACT	
													YES	NO
		2	9	6	6	1	7		12/88	European				
	8	3	0	2	3	5	7		7/83	PCT				
		2	7	9	2	2	9		8/88	European				

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

	Casey, et al, "Digital Signal Processing IC Helps to Shed New Light on Image Processing Applications", Electronic Design, Penton Publishing, Cleveland, OH March 20, 1986, pp. 135-140.

EXAMINER

DATE CONSIDERED

EXAMINER: Initial if citation is considered, draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

(Form PTO-1449 [6-4])

FORM PTO-1449 U.S. Department of
Commerce(Rev. 4/92) Patent and Trademark Office

**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

(Use several sheets if necessary)

ATTY. DOCKET NO.

566.28851CX2

SERIAL NO.

Not yet assigned

APPLICANT
YAMADA, et al

FILING DATE
March 12, 2004

GROUP
Not yet assigned

U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER								DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE	
		DOCUMENT NUMBER								DATE	COUNTRY	CLASS	SUBCLASS	ABSTRACT	
														YES	NO
		6	4	5	9	1	7	3	3/89	Japan				Eng.Tran	
	6	3	2	0	0	2	3	2	8/88	Japan				Eng.Tran	
	6	4	5	9	4	5	1		3/89	Japan				Eng.Tran	
	6	3	1	4	8	2	9	2	6/88	Japan				Eng.Tran	

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

EXAMINER

DATE CONSIDERED

EXAMINER: Initial if citation is considered, draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

(Form PTO-1449 [6-4])